source driver, etc.), a third source driver unit **630** (e.g., a third source driver, etc.), a fourth source driver unit **640** (e.g., a fourth source driver, etc.), and a timing controller **650**. The timing controller **650** may include a first port output terminal **651**, a second port output terminal **652**, a third port output terminal **653**, and a fourth port output terminal **654**. The timing controller **650** may control the first port output terminal **651** so that output data is transmitted from the first port terminal **651** to the second source driver unit **620** at a first transmission speed. The timing controller **650** may also control the second port output terminal **652** so that output data is transmitted from the second port terminal **652** to the third source driver unit **630** at a second transmission speed. Also, the timing controller **650** may control the third port output terminal **653** so that output data is transmitted from the third port terminal **653** to the first source driver unit **610** at a third transmission speed. Further, the timing controller **650** may control the fourth port output terminal **654** so that output data is transmitted from the fourth port terminal **654** to the fourth source driver unit **640** at a fourth transmission speed. The timing controller **650** may differently control an output data transmission speed according to a vertical or horizontal distance between each source driver unit and the timing controller **650**. In an exemplary embodiment, the first transmission speed and the second transmission speed may be equal to each other since a vertical or horizontal distance between the second source driver unit **620** and the timing controller **650** is equal to that between the third source driver unit **630** and the timing controller **650**. In addition, the third transmission speed and the fourth transmission speed may be equal to each other since a vertical or horizontal distance between the first source driver unit **610** and the timing controller **650** is equal to that between the fourth source driver unit **640** and the timing controller **650**. However, since the vertical or horizontal distance between the first source driver unit **610** and the timing controller **650** is greater than that between the second source driver unit **620** and the timing controller **650**, the timing controller **650** may be controlled so that the first transmission speed is higher than the third transmission speed. Likewise, since the vertical or horizontal distance between the fourth source driver unit **640** and the timing controller **650** is greater than that between the third source driver unit **630** and the timing controller **650**, the timing controller **650** may be controlled so that the second transmission speed is higher than the fourth transmission speed.

[0109] Referring to FIG. **12**, the second source driver unit **620** and the third source driver unit **630** may have the same configuration. That is, the second source driver unit **620** and the third source driver unit **630** may include the same type of source drivers (for example, source drivers supporting the same transmission speed) and the same number of source drivers, and dispositions on PCBs may be the same. In addition, the first source driver unit **610** and the fourth source driver unit **640** may have the same configuration, thereby providing a symmetrical structure centered on the timing controller **650**. Furthermore, the display driving device **600** may include more source drivers and form a symmetrical structure.

[0110] FIG. **13** is a diagram illustrating a display driving device **700** according to another exemplary embodiment.

[0111] Referring to FIG. **13**, the display driving device **700** may have an asymmetrical structure centered on a timing controller **750**, unlike the display driving device **600** of FIG. **12**. That is, based on the number of source drivers, a second source driver unit **720** and a third source driver unit **730** may be different in configuration, and a first source driver unit **710** and a fourth source driver unit **740** may be different in configuration. Thus, the display driving device **700** may have an asymmetrical structure. However, the inventive concept is not limited thereto, and the display driving device **700** may have an asymmetrical structure due to various structural characteristics such as disposition positions of source drivers on a PCB.

[0112] FIG. **14** is an exploded perspective view illustrating a display module **2100** according to an exemplary embodiment.

[0113] Referring to FIG. **14**, the display module **2100** may include a display device **1000** of FIG. **1**, a polarizing plate **2110**, and a window glass **2120**. The display device **1000** may include a display panel **200**, a printed board **300**, and a display driving device **100**.

[0114] The window glass **2120** is generally formed of a material such as acryl or tempered glass, and the window glass **2120** may protect the display module **2100** from being scratched due to a repeated touch or an external impact. The polarizing plate **2110** may be provided to improve optical characteristics of the display panel **200**. The display panel **200** may be patterned and formed as a transparent electrode on the printed board **2120**. The display panel **200** may include a plurality of pixel cells for displaying a frame. The display panel **200** may be an organic light-emitting diode panel. Each of the pixel cells may include an organic light-emitting diode that emits light in response to the flow of current. However, the present exemplary embodiment is not limited thereto, and the display panel **200** may include any of diverse display elements. For example, the display panel **200** may be one of an LCD panel, an electrochromic display (ECD) panel, a digital mirror device (DMD), an actuated mirror device (AMD), a grating light valve (GLV), a plasma display panel (PDP), an electro luminescent display (ELD) panel, a light-emitting diode (LED) display panel, and a vacuum fluorescent display (VFD) panel.

[0115] The display driving device **100** may be the display driving device **100** of FIG. **1**. Although the display driving device **100** in FIG. **14** is one chip for convenience of explanation, the present exemplary embodiment is not limited thereto, and the display driving device **100** may be mounted as a plurality of chips. Also, the display driving device **100** may be mounted as a chip-on-glass (COG) type on the printed board **300** formed of glass material. However, the present exemplary embodiment is not limited thereto, and the display driving device **100** may be mounted as any of various types such as a chip-on-film (COF) type or a chip-on-board (COB) type.

[0116] The display module **2100** may further include a touch panel **2130** and a touch controller **2140**. The touch panel **2130** may be formed by patterning a transparent electrode such as an electrode formed of indium tin oxide (ITO) on a glass substrate or a polyethylene terephthalate (PET) film. The touch controller **2140** may detect a touch on the touch panel **2130**, may calculate coordinates of the touch, and may transmit the coordinates to a host (not shown). The touch controller **2140** may be integrated with the display driving device **100** into one semiconductor chip.

[0117] FIG. **15** is a block diagram of a display system **2200** according to an exemplary embodiment. Referring to FIG. **158**, the display system **2200** may include a processor **2220**,